

WavePro® 7 Zi Series

1.5 GHz - 6 GHz



Vertical System	WavePro 715Zi	WavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Z (SDA, DDA)
Analog (ProLink Input) Bandwidth @ 50 Ω (-3 dB) (≥ 10 mV/div)	Not Applicable	Not Applicable	Not Applicable	4 GHz (≥ 10 mV/div)	6 GHz (≥ 10 mV/div)
Analog (ProBus Input) Bandwidth	1.5 GHz	2.5 GHz	3.5 GHz	3.5 GHz	3.5 GHz
2 50 Ω (-3 dB)	(≥ 10 mV/div)	(≥ 10 mV/div)	(≥ 10 mV/div)	(≥ 10 mV/div)	(≥ 10 mV/div)
Analog (ProBus Input) Bandwidth 1 MΩ (-3 dB)	500 MHz (Typical)	500 MHz (Typical)	500 MHz (Typical)	500 MHz (Typical)	500 MHz (Typical)
Rise Time (Typical, 50 Ω)	235 ps	150 ps	120 ps	105 ps	70 ps
nput Channels	4		- 1		
Bandwidth Limiters	20 MHz, 200	MHz, 1 GHz	20 MHz, 200 MHz 1 GHz, 3 GHz	20 MHz, 200 MHz 1 GHz, 3 GHz	20 MHz, 200 MHz 1 GHz, 3 GHz, 4 GH
nput Impedance	50 Ω ±2% or 1 MΩ II	16 pF, 10 MΩ 11 pF \		, , , ,	_ , ,
nput Coupling	1 MΩ: AC, DC, GND;				
Maximum Input Voltage		50 Ω: ±5 V _{rms}) V max. (peak AC: ≤ 10) kHz + DC)	50Ω (ProBus): ±5 V _{rms} 50Ω (ProLink): ±4 V _{peak} 1 MΩ (ProBus): 250 V max. (peak AC: ≤ 10 kHz + DC)	
Channel-Channel Isolation	≥ 100:1 at 2 GHz; ≥ 40	0:1 at 3 GHz; ≥ 20:1 at 4	4 GHz	·	
Vertical Resolution		rith enhanced resolution			
Sensitivity		ully variable (2–9.99 mV	<u> </u>	2 mV–10 V/div, fully vai	iable
OC Gain Accuracy	±1.5% of full scale	, , , , , , , , , , , , , , , , , , , ,	.,,	, . , ,	
Offset Range	50 Ω (ProBus Input): ±750 mV @ 10–170 mV/div ±4 V @ 172 mV/div-1 V/div 1 MΩ: (ProBus Input): ±1 V @ 2–128 mV/div ±10 V @ 130 mV-1.28 V/div ±100 V @ 1.3 V-10 V/div			50 Ω (ProLink Input): ± 750 mV @ 10–118 mV/div ± 4 V @ 120 mV/div–1 V/div 50 Ω (ProBus Input): ± 750 mV @ 10–170 mV/div ± 4 V @ 172 mV/div–1 V/div 1 MΩ: (ProBus Input): ± 1 V @ 2–128 mV/div ± 10 V @ 130 mV–1.28 V/div ± 100 V @ 1.3 V–10 V/div	
Offset Accuracy	±(1.5% of full scale +	1.0% of offset value +	1 mV)		,
·					
Iorizontal System					
Horizontal System	Internal timebase con	amon to 4 input channe	yle: an aytarnal alaak m	ay be applied at the au	viliany input
limebases		nmon to 4 input channe			
īmebases īme/Division Range	Real time: 20 ps/div–1	000 s/div (RIS mode: 2	0 ps/div-10 ns/div; Roll		
īmebases īme/Division Range Clock Accuracy	Real time: 20 ps/div-1 ≤ 1 ppm + (aging of 0	000 s/div (RIS mode: 2 .5 ppm/yr from last cali	0 ps/div–10 ns/div; Roll bration)		
imebases ime/Division Range Clock Accuracy ime Interval Accuracy	Real time: 20 ps/div−1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms	0 ps/div–10 ns/div; Roll bration) s)	mode: up to 1000 s/di	v)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical)	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical)	0 ps/div-10 ns/div; Roll bration) s) 800 fs (Typical)	mode: up to 1000 s/di	v) 560 fs (Typical)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor rigger and Interpolator Jitter	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical)	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms}	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical)	mode: up to 1000 s/di	v)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor rigger and Interpolator Jitter Channel-Channel Deskew Range	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting,	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} 100 ms max., each ch	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel	mode: up to 1000 s/di	v) 560 fs (Typical)
Finebases Fine/Division Range Clock Accuracy Fine Interval Accuracy Clitter Noise Floor Finger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output)	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 × time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms}	0 ps/div=10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output	mode: up to 1000 s/di 750 fs (Typical) 1 ps _{rms}	v) 560 fs (Typical)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor irigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 × time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{ms} . 100 ms max., each ch ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance,	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi	750 fs (Typical) 1 ps _{rms} input WP740Zi	560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor irigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 × time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} . 100 ms max., each ch ance, applied at the real	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary	750 fs (Typical) 1 ps _{rms}	v) 560 fs (Typical) (Typical)
Timebases Time/Division Range Clock Accuracy Time Interval Accuracy Jitter Noise Floor Trigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output)	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{ms} . 100 ms max., each ch ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance,	0 ps/div-10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi	560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor irigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquistion System Single-Shot Sample Rate/Ch	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 § WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate)	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} .100 ms max., each ch ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance, WP725Zi (SDA)	0 ps/div=10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch	v) 560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor frigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquistion System Single-Shot Sample Rate/Ch	Real time: 20 ps/div-1 1 ppm + (aging of 0 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) 49 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz-100 MHz, 50 S WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitive.	000 s/div (RIS mode: 2 .5 ppm/yr from last caliccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} .100 ms max., each chance, applied at the realance, applied at the realance, applied at the realance, applied at the seance, applied at the realance, applied at the realance applied at the realance, applied at t	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch	v) 560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor frigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquistion System Single-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate	Real time: 20 ps/div-1 1 ppm + (aging of 0 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) 49 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz-100 MHz, 50 S WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitive.	000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} .100 ms max., each ch ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance, WP725Zi (SDA)	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch	560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor rigger and Interpolator Jitter Channel-Channel Deskew Range external Timebase Reference (Input) external Timebase Reference (Output) external Clock Acquistion System Single-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate Intersegment Time	Real time: 20 ps/div-1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz-100 MHz, 50 Ω WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitivi 1,250,000 waveforms 800 ns	000 s/div (RIS mode: 2 .5 ppm/yr from last caliccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} .100 ms max., each chance, applied at the realance, applied at the realance, applied at the realance, applied at the seance, applied at the realance, applied at the realance applied at the realance, applied at t	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	mode: up to 1000 s/di 750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch	V) 560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor frigger and Interpolator Jitter Channel-Channel Deskew Range external Timebase Reference (Input) external Timebase Reference (Output) external Clock Acquistion System Single-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate Intersegment Time Maximum Acquisition Memory Points/Ch	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 g WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv. 1,250,000 waveforms 800 ns (4 Ch / 2 Ch)	000 s/div (RIS mode: 2 .5 ppm/yr from last calicuracy* Reading) (rms 1 ps (Typical) 2 psms 100 ms max., each chance, applied at the real ance, applied at the real ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance, WP725Zi (SDA)	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	mode: up to 1000 s/di 750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch	V) 560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy litter Noise Floor frigger and Interpolator Jitter Channel-Channel Deskew Range external Timebase Reference (Input) external Timebase Reference (Output) external Clock Acquistion System Single-Shot Sample Rate/Ch Maximum Trigger Rate Intersegment Time Maximum Acquisition Memory Points/Ch Standard Memory	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 g WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv. 1,250,000 waveforms 800 ns (4 Ch / 2 Ch)	000 s/div (RIS mode: 2 .5 ppm/yr from last caliccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} .100 ms max., each chance, applied at the realance, applied at the realance, applied at the realance, applied at the seance, applied at the realance, applied at the realance applied at the realance, applied at t	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	mode: up to 1000 s/di 750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch Number of	V) 560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
Timebases Time/Division Range Clock Accuracy Time Interval Accuracy Timebase Floor Trigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquistion System Single-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate Intersegment Time Maximum Acquisition Memory Points/Ch Standard Memory S-32 – Memory Option	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 Ω WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv. 1,250,000 waveforms 800 ns (4 Ch / 2 Ch) 10 M / 20 M (Standard 32 M / 64 M	000 s/div (RIS mode: 2 .5 ppm/yr from last calicuracy* Reading) (rms 1 ps (Typical) 2 psms 100 ms max., each chance, applied at the real ance, applied at the real ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance, WP725Zi (SDA)	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	750 fs (Typical) 750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch Number of / 40 M) 5000 15,000	V) 560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
Timebases Time/Division Range Clock Accuracy Time Interval Accuracy Jitter Noise Floor Trigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquistion System	Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 g WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv. 1,250,000 waveforms 800 ns (4 Ch / 2 Ch) 10 M / 20 M (Standard	000 s/div (RIS mode: 2 .5 ppm/yr from last calicuracy* Reading) (rms 1 ps (Typical) 2 psms 100 ms max., each chance, applied at the real ance, applied at the real ance, applied at the real ance, applied at the real Ω or 1 MΩ impedance, WP725Zi (SDA)	0 ps/div–10 ns/div; Roll bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	mode: up to 1000 s/di 750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch Number of	V) 560 fs (Typical) (Typical) WP760Zi (SDA, DDA)

Acquisition Processing	WavePro 715Zi	WavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Zi (SDA, DDA)	
Averaging	Summed averaging to 1 million sweeps; continuous averaging to 1 million sweeps					
Enhanced Resolution (ERES)	From 8.5 to 11 bits ve					
Envelope (Extrema)	Envelope, floor, or roof for up to 1 million sweeps					
Interpolation	Linear or Sin x/x					
Triggering System						
Modes	Normal, Auto, Single,	and Stop				
Sources	Any input channel, Au	x, Aux/10, or line; slope	and level unique to e	ach source (except line	trigger)	
Coupling Mode	DC, AC, HFRej, LFRej					
Pre-trigger Delay		ize (adjustable in 1% in				
Post-trigger Delay	0-10,000 divisions in real time mode, limited at slower time/div settings or in roll mode					
Hold-off by Time or Events	From 2 ns up to 20 s	or from 1 to 99,999,999	events			
Internal Trigger Range	±4.1 div from center					
Trigger Sensitivity with	2 div @ < 1 GHz	2 div @ < 2.5 GHz		2 div @ < 3.5 GHz		
Edge Trigger (Ch 1–4) ProBus Inputs	1.5 div @ < 500 MHz	1.5 div @ < 1.25 GHz		1.5 div @ < 1.75 GHz		
	1.0 div @ < 200 MHz (for DC, AC,	1.0 div @ < 200 MHz (for DC, AC,	(for DC AC	1.0 div @ < 200 MHz , LFRej coupling, ≥ 10 m\	//div_50.0.)	
	LFRej coupling,	LFRei coupling,	(IOI DC, AC	, Li Hej coupling, 2 To m	v/uiv, 50 <u>52</u> /	
	\geq 10 mV/div, 50 Ω)	\geq 10 mV/div, 50 Ω)				
Trigger Sensitivity with				2 div @ < 4 GHz	2 div @ < 6 GHz	
Edge Trigger (Ch 1-4) ProLink Inputs				1.5 div @ < 2 GHz	1.5 div @ < 3 GHz	
		Not Applicable		1.0 div @ < 200 MHz	1.0 div @ < 200 MH	
				(for DC, AC,	(for DC, AC,	
				LFRej coupling, \geq 10 mV/div, 50 Ω)	LFRej coupling, \geq 10 mV/div, 50 Ω)	
External Trigger Sensitivity,	2 div @ < 1 GHz			≥ 10 111V/GIV, 50 \$2 }	≥ 10 111V/UIV, 50 ½2)	
(Edge Trigger)	1.5 div @ < 500 MHz					
(2090990.)	1.0 div @ < 200 MHz					
	(for DC, AC, LFRej co	upling)				
Max. Trigger Frequency, SMART Trigger™	1.0 GHz @ ≥ 10 mV/div	2.0 GHz @ ≥ 10 mV/div	2.0 GHz @ ≥ 10 mV/div	2.0 GHz @ ≥		
	(minimum triggerable		(minimum triggerable	(minimum triggera	ble width 200 ps)	
- IT. I I I	width 500 ps)	width 300 ps)	width 250 ps)			
External Trigger Input Range	Aux (±0.4 V); Aux/10 (±	:4 V)				
Basic Triggers						
Edge		eets slope (positive, neg				
TV-Composite Video		vith selectable line and fie				
		with selectable Fields (1-		Frame Rates (25, 30, 50,	or 60 Hz), Interlacing	
Window	(1:1, 2:1, 4:1, 8:1), or Synch Pulse Slope (Positive or Negative). Trigger when signal or exits a window defined by adjustable thresholds.					
			, ,			
SMART Triggers State or Edge Qualified	Triggers on any innut.	acuras apluif a dafinad	atata ar adaa aaaurra	d an another innut cour		
State of Edge Qualified		source only if a defined es is selectable by time		a on another input sour	ce.	
Qualified First		on mode, triggers repea				
		gment of the acquisitio			me or events.	
Dropout	Triggers if signal drops out for longer than selected time between 1 ns and 20 s. Logic combination (AND, NAND, OR, NOR) of 5 inputs (4 channels and external trigger input).					
Pattern						
	Triggers at start or end	gh, low, or don't care. I	ne High and Low leve	ei can be selected indep	endently.	
		d of the pattern.				
	ochnology					
				ow as 200 no Idanandir	na on oscilloscope	
	Triggers on positive or	negative glitches with	widths selectable as I	ow as 200 ps (depending	ig on oscilloscope	
Glitch	Triggers on positive or bandwidth) to 20 s, or	on intermittent faults.				
Glitch	Triggers on positive or bandwidth) to 20 s, or Triggers on positive, n					
SMART Triggers with Exclusion T Glitch Width (Signal or Pattern) Interval (Signal or Pattern)	Triggers on positive or bandwidth) to 20 s, or Triggers on positive, n bandwidth) to 20 s, or	on intermittent faults. egative or both widths	with widths selectable			
Glitch Width (Signal or Pattern) Interval (Signal or Pattern)	Triggers on positive or bandwidth) to 20 s, or Triggers on positive, n bandwidth) to 20 s, or Triggers on intervals s	on intermittent faults. egative or both widths on intermittent faults.	with widths selectable and 20 s.	e as low as 200 ps (dep	ending on oscilloscop	
Glitch Width (Signal or Pattern)	Triggers on positive or bandwidth) to 20 s, or Triggers on positive, n bandwidth) to 20 s, or Triggers on intervals s Triggers on any source	on intermittent faults. egative or both widths on intermittent faults. electable between 1 ns	with widths selectable and 20 s.	e as low as 200 ps (dep	ending on oscilloscop	
Glitch Width (Signal or Pattern) Interval (Signal or Pattern)	Triggers on positive or bandwidth) to 20 s, or Triggers on positive, n bandwidth) to 20 s, or Triggers on intervals s Triggers on any source Delay between source Trigger on positive or	on intermittent faults. egative or both widths on intermittent faults. electable between 1 ns e if a given state (or trar es is 1 ns to 20 s, or 1 to negative runts defined I	with widths selectable and 20 s. nsition edge) has occu o 99,999,999 events.	e as low as 200 ps (dep	ending on oscilloscop	
Glitch Width (Signal or Pattern) Interval (Signal or Pattern) Timeout (State/Edge Qualified)	Triggers on positive or bandwidth) to 20 s, or Triggers on positive, n bandwidth) to 20 s, or Triggers on intervals s Triggers on any source Delay between source Trigger on positive or Select between 1 ns a	on intermittent faults. egative or both widths on intermittent faults. electable between 1 ns e if a given state (or trar es is 1 ns to 20 s, or 1 to negative runts defined I	with widths selectable and 20 s. nsition edge) has occu o 99,999,999 events. by two voltage limits a	e as low as 200 ps (deported on another source.	ending on oscilloscop	

High-speed Serial Protocol Triggering	WavePro 715Zi	WavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Zi (SDA, DDA)
Data Rates	Not available	the state of the s	SPT, standard with b/s–1.25 Gb/s	· · · · · · · · · · · · · · · · · · ·	HSPT, standard with Mb/s–2.7 Gb/s
Pattern Length	_			JRZ or 8b10b	
Clock and Data Outputs	_			ical), AC coupled	
Clock Recovery Jitter	_	1 ps rms + 0.3% l	Jnit Interval rms for PR		50% transition density
Hardware Clock Recovery Loop BW	-		BW = Fbaud/5500, 50		
Low-speed Serial Protocol Triggering (Optional)					
Available		OP), UART-RS232, CA datasheets for comple			
Color Waveform Display					
Туре			with high resolution t	ouch screen	
Resolution	WXGA; 1280 x 768 p				
Number of Traces			usly display channel, z	oom, memory and ma	th traces.
Grid Styles	Auto, Single, Dual, Q	uad, Octal, X-Y, Single-	+X-Y, Dual+X-Y		
Waveform Representation	Sample dots joined, o	or sample dots only			
Integrated Second Display					
Туре	Color 15.3" flat panel	TFT-Active Matrix LCD	with high resolution t	ouch screen	
Resolution	WXGA; 1280 x 768 p		<u> </u>		
LeCroy WaveStream Fast Viewing Mode					
Intensity	256 Intensity Levels.	1–100% adjustable via	a front panel control		
Number of Channels	Up to 4 simultaneous		'		
Туре	Select analog or color				
Max. Sampling Rate			ıt WPZi-1.5GHZ-4X200	S ontion)	
Persistence Aging	Select from 500 ms t		10 001 21 1.00112 17.200	10 option,	
Waveforms/Second (continuous)	Up to 2500 Waveform				
Analog Persistence Display	Op to 2000 (vavoron)	no, occorra			
Analog and Color-Graded Persistence	Variable saturation lev	els; stores each trace	's persistence data in r	nemory	
Persistence Types	Select analog, color, o				
Trace Selection	Activate persistence	on all or any combinati	on of traces		
Persistence Aging	Select from 500 ms t	o infinity			
Sweep Display Modes	All accumulated, or al	l accumulated with las	st trace highlighted		
High-speed Digitizer Output (Opt	tion)				
Type	LeCroy LSIB				
Transfer Rate	Up to 500 Mpts/s (Ma	aximum)			
Output Protocol	PCI Express, Gen1 (4	lanes utilized for data	transfer)		
Control Protocol	TCP/IP				
Command Set	Via Windows Automa	ition, or via LeCroy Re	mote Command Set		
Zoom Expansion Traces					
	Display up to 4 Zoom	and 8 Math/Zoom tra	ces		
Processor/CPU					
Type	Intel® Core™ 2 Quad, 2	2.5 GHz (or better)			
Processor Memory	2 GB standard, up to		-t	" 100"	
			standard with "M-64"	or L-128 memory)	
Operating Cust	IVIICIOSOTT VVINDOWS®	Vista® Business Editio			
Operating System Real Time Clock	Date and time display	ed with waveform and	d in hardcopy files		
Operating System Real Time Clock	Date and time display SNTP support to sync	red with waveform and chronize to precision in			
Real Time Clock	SNTP support to sync	chronize to precision in emory traces (M1–M4	nternal clocks.) store 16-bit/point full		
Real Time Clock	SNTP support to sync	chronize to precision in emory traces (M1–M4	iternal clocks.		capacity.

Interface	WavePro 715Zi	NavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Zi (SDA, DDA)
Remote Control	Via Windows Autom	nation, or via LeCrov	Remote Command Set	-	
Network Communication Standard	LXI Class C, VXI-11				
GPIB Port (Optional)	Supports IEEE – 488				
_SIB Port (Optional)			with LeCroy supplied Al	Pl	
JSB Device Port (Optional)	Туре-В	se com x i protector	Tital Eddie y dapphour in		
Ethernet Port	Supports 10/100/10	NORaseT Ethernet in	terface (B I45 port)		
JSB Ports) USB 2.0 ports support	Windows compatible	Havidas
External Monitor Port			oort customer-supplied		
External Monitor Fort	support LeCroy WP:	Zi-EXTDISP-15 addit	onal touch screen displa or other second monitor	ay accessory. Includes :	
Peripheral Bus	LeCroy LBUS stand				
Auxiliary Input					
Signal Types			Input on the front pane		
Coupling	50 Ω: DC; 1 MΩ: AC				
Max. Input Voltage	50 Ω: 5 V _{rms} ; 1 MΩ:	250 V (Peak AC < 1	0 kHz + DC)		
Auxiliary Output					
Signal Types	Select from calibrate				
Calibrator Signal			; 0.0 to 500 mV into 50 s	Ω (0–1 V into 1 MΩ)	
Control Signals	Trigger enabled, trig	ger out, pass/fail sta	tus		
Automatic Setup					
Auto Setup			d sensitivity to display a		
Find Vertical Scale	Automatically sets t with the maximum		y and offset for the selec	cted channel to display	a waveform
General					
Auto Calibration	Ensures specified D	C and timing accura	cy is maintained for 1 ye	ear minimum.	
Probes					
Probes	Qty. (4) ÷10 Passive	Probes			
Probe System	of compatible probe	S	dels). Automatically dete	ects and supports a vari	ety
Scale Factors			ending on probe used		
Calibration Output	1 kHz square wave,	1 V _{p-p} (typical), outp	ut to probe hook		
Power Requirements					
Voltage		at 50/60 Hz; 100–1	20 VAC ±10% at 400 Hz	z; Automatic AC Voltage	e Selection
Max. Power Consumption	800 W/ 800 VA				
Environmental					
Temperature (Operating)	+5 °C to +40 °C incl	uding CD-RW/DVD-	ROM drive		
Temperature (Non-Operating)	-20 °C to +60 °C	1 2 12 7			
Humidity (Operating)			ensing) up to +31 °C.		
Lungidity (Nan Ongetical)			idity (Non-condensing) a		
Humidity (Non-Operating)	Up to 10,000 ft. (304		ensing) as tested per MI	L-F NF-Z 0 0 U F	
Altitude (Operating) Altitude (Non-Operating)	Up to 10,000 ft. (304 Up to 40,000 ft. (12,		20 6		
			ach of three orthogonal	0.700	
Random Vibration (Operating) Random Vibration (Non-Operating)			ach of three orthogonal ach of three orthogonal		DDE 20000E
Functional Shock		11 ms pulse, 3 shock	s (positive and negative)		
Physical Dimensions					
Dimensions (HWD)	355 mm x 467 mm x	289 mm· 14" x 18 4	" x 11.4" (height excludes	feet)	
Weight	18.4 kg; 40 lbs.	(200 mm, 14 X 10.4	moight oxolddos		
Shipping Weight	26.6 kg; 58 lbs.				
Certifications					
	CE Compliant, UL a CSA C22.2 No. 610		rms to EN 61326, EN 61	010-1, UL 61010 2nd e	dition, and
Warranty and Service					
	3-year warranty; cali upgrades, and calibr		ed annually. Optional sei	rvice programs include	extended warranty,

Standard

Math Tools

Display up to 8 math function traces (F1-F8). The easy-to-use graphical interface simplifies setup of up to two operations on each function trace, and function traces can be chained together to perform math-on-math.

absolute value invert (negate) average (summed) log (base e) log (base 10) average (continuous) derivative product (x) deskew (resample) ratio (/) difference (-) reciprocal rescale (with units) enhanced resolution (to 11 bits vertical)

envelope roof exp (base e) (sinx)/x exp (base 10) square fft (power spectrum, magnitude, phase, square root up to 128 Mpts) sum(+)floor zoom (identity)

integral

- Parameter math add, subtract, multiply, or divide two different parameters
- Narrow-band power measurements
- Auto-correlation function
- Sparse function
- Cubic and Quadratic Interpolation function

Measure Tools

Display any 12 parameters together with statistics, including their average, high, low, and standard deviations. Histicons provide a fast, dynamic view of parameters and wave shape characteristics.

amplitude level@x rms maximum std. deviation area mean base ton cycles median width data minimum median narrow band phase phase delay narrow band power time @ minimum (min.) ∆ delay number of points time @ maximum (max.) duty cycle duration +overshoot Δ time @ level falltime (90-10%, -overshoot Δ time @ level from 80-20%, @ level) trigger peak-to-peak frequency x@ max. period first x@ min. risetime (10-90%.

Pass/Fail Testing

last

Simultaneously test multiple parameters against selectable parameter limits or pre-defined masks. Pass or fail conditions can initiate actions including document to local or networked files, e-mail the image of the failure, save waveforms, send a pulse out at the front panel auxiliary BNC output, or (with the GPIB option) send a GPIB SRQ.

20-80%, @ level)

Standard

Jitter and Timing

Parametric Measurements:

- period@level width@level duty@level frequency@level
- TIE@level edge@level

Statistical Analysis:

Jitter Trend (1000 pts) • Histograms (1000 pts)

Software Options

Jitter and Timing Analysis Software Package (WPZi-JTA2)

This package provides jitter timing and analysis using time, frequency, and statistical views for common timing parameters, and also includes other useful tools. JTA2 includes:

- Jitter and timing parameters, with "Track" graphs of
- Cycle-Cycle Jitter - Period - Hold - Half Period - Skew - N-Cycle - N-Cycle with start - Width - Duty Cycle selection - Time Interval Error - Duty Cycle Error - Frequency
- Edge@lv parameter (counts edges)
- Histograms expanded with 19 histogram parameters and up to 2 billion events

- Setup

- Trend (datalog) of up to 1 million events
- Track graphs of all parameters
- Persistence histogram, persistence trace (mean, range, sigma)

Spectrum Analyzer Mode (WPZi-SPECTRUM)

This package provides a new capability to navigate waveforms in the frequency domain using spectrum analyzer type controls.

FFT capability added to include:

- power averaging power density real and imaginary components
- frequency domain parameters FFT on up to 128 Mpts.

Disk Drive Measurements Package (WPZi-DDM2)

This package provides disk drive parameter measurements and related mathematical functions for performing disk drive WaveShape Analysis.

• Disk Drive Parameters are as follows:

amplitude assymetry local time trough-peak local base local time under threshold local baseline separation narrow band phase local maximum narrow band power local minimum overwrite local number pulse width 50 local peak-peak pulse width 50local time between events pulse width 50+ resolution local time between peaks local time between troughs track average amplitude local time at minimum track average amplitudelocal time at maximum track average amplitude+ auto-correlation s/n local time peak-trough local time over threshold non-linear transition shift

ORDERING INFORMATION

Product Description	Product Code	Product Description Product Code
WavePro 7 Zi Series Oscilloscopes		Memory and Sample Rate Options (cont'd)
1.5 GHz, 10 GS/s, 4 Ch, 10 Mpts/Ch (20 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 715Zi	64 Mpts/Ch (128 Mpts/Ch Interleaved) Memory Option WPZi-M-6 for WavePro 7 Zi. Includes an additional 6 GB of RAM (8 GB total)
2.5 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 725Zi	64 Mpts/Ch (128 Mpts/Ch Interleaved) Memory Option DDAZi-M-6 for DDA 7 Zi. Includes an additional 6 GB of RAM (8 GB total)
3.5 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 735Zi	64 Mpts/Ch (128 Mpts/Ch Interleaved) Memory Option SDAZi-M-6 for SDA7 Zi. Includes an additional 6 GB of RAM (8 GB total)
4 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 740Zi	128 Mpts/Ch (256 Mpts/Ch Interleaved) Memory Option WPZi-L-12 for WavePro 7 Zi. Includes an additional 6 GB of RAM (8 GB total)
6 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 760Zi	128 Mpts/Ch (256 Mpts/Ch Interleaved) Memory Option DDAZi-L-12 for DDA 7 Zi. Includes an additional 6 GB of RAM (8 GB total)
SDA Zi Series Serial Data Analyzers 2.5 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch	SDA 725Zi	128 Mpts/Ch (256 Mpts/Ch Interleaved) Memory Option SDAPZi-L-12 for SDA 7 Zi. Includes an additional 6 GB of RAM (8 GB total)
(40 GS/s and 40 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	SDA 735Zi	20 GS/s (40 GS/s Interleaved) Sampling Rate WPZi-1.5GHZ-4X20G Option for 1.5 GHz WavePro 715 Zi
3.5 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input		CPU, Computer and Other Hardware Options Upgrade from 2 GB to 8 GB CPU RAM WPZi-2-UPG-8GBRAN
4 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	SDA 740Zi	Upgrade from 4 GB to 8 GB CPU RAM Upgrade from Standard Size Hard Drive to 200 GB Hard Drive WPZi-4-UPG-8GBRAN WPZi-4-UPG-8GBRAN WPZi-200GB-HI
6 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	SDA 760Zi	Additional 80 GB Hard Drive WPZi-80GB-RHD-0 Additional 200 GB Hard Drive WPZi-200GB-RHD-0
DDA 7 Zi Series Oscilloscopes		GPIB Option for LeCroy Oscilloscope GPIB- USB Device Input. Half-height Card with Type-B Connector USBDEVICE-
3.5 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode)	DDA 735Zi	Serial Data Options and Accessories
with 50 Ω and 1 M Ω Input 6 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode)	DDA 760Zi	2.7 Gb/s High-speed Serial Pattern Trigger Option WPZi-HSP for 4–6 GHz Oscilloscopes (Standard on SDA 7 Zi and DDA 7 Zi)
with 50 Ω and 1 M Ω Input Included with Standard Configuration		1.25 Gb/s Medium-speed Serial Pattern Trigger Option WPZi-MSP for 2.5–3.5 GHz Oscilloscopes (Standard on SDA 7 Zi and DDA 7 Zi)
÷10, 500 MHz Passive Probe (Qty. 4)		Cable De-Embed (Standard on SDA7 Zi WPZi–CBL-DE-EMBEI
ProLink to SMA Adapter: 4 each Optical 3-Button Wheel Mouse, USB 2.0	LPA-SMA-A	and DDA 7 Zi) 8b10b Decode only Option (Standard on SDA 7 Zi WPZi-8B10B I and DDA 7 Zi)
Protective Front Cover Printed Quick Reference Guide		I ² C Bus Trigger and Decode Option WPZi-I2Cbus TI
Printed Galek Noterence Galact Printed Getting Started Manual		SPI Bus Trigger and Decode Option WPZi-SPIbus TI
Product Manual Set on CD-ROM		LIN Trigger and Decode Option WPZi-LINbus TI
Norton Anti-virus Software (Trial Version)		UART and RS-232 Trigger and Decode Option WPZi-UART-RS232bus TI
Microsoft Windows® Vista® License		FlexRay Trigger and Decode Option WPZi-FlexRayBus TI
Commercial NIST Calibration with Performance Certification	nte	FlexRay Bus Trigger, Decode, and Physical Layer WPZi-FlexRayBus TD
Power Cable for the Destination Country		Test Option
3-year Warranty		CANbus TDM Trigger, Decode and WPZi-CANbus TDM Measure/Graph Option CANbus TD Trigger and Decode Option WPZi-CANbus TI
Memory and Sample Rate Options 32 Mpts/Ch (64 Mpts/Ch Interleaved) Memory Option	M/D7; C 22	Ethernet Application Software QPHY-ENET
for WavePro 7 Zi. Includes an additional 2 GB of RAM (4 GB total)	WPZi-S-32	USB Application Software USB Application Software OPHY-USB PCle Gen1 Compliance and Development Software Package OPHY-PCl
32 Mpts/Ch (64 Mpts/Ch Interleaved) Memory Option for DDA 7 Zi. Includes an additional 2 GB of RAM (4 GB total)	DDAPZi-S-32	QualiPHY Enabled SATA Software Option QPHY-SAT. WiMedia UWB Transmitter Measurement Software Option QPHY-UWI
32 Mpts/Ch (64 Mpts/Ch Interleaved) Memory Option	SDAZi-S-32	Eye Doctor (Virtual Probe and Equalizer emulation) Bundle WPZi-EYEDI Eye Doctor Virtual Probing Element WPZi-EYEDR-V
for SDA 7 Zi. Includes an additional 2 GB of RAM (4 GB total)		Eye Doctor Virtual Probing Element WPZI-EYEDR-V Eye Doctor Equalized Receiver Emulation WPZI-EYEDR-E(
		*TF-ENET-B required. [†] TF-USB-B required.

ORDERING INFORMATION

Product Description	Product Code	Product Description Pr	oduct Code
High-speed Digitizer Output		Probes and Probe Accessories	
High-speed PCle Gen1 x4 Digitizer Output	LSIB-1	2.5 GHz, 0.7 pF Active Probe (÷10), Small Form Factor	HFP2500
PCI Express X4 Host Interface Board for Desktop PC	LSIB-HOSTBOARD	1.5 GHz, 0.9 pF, 1 MΩ High Impedance Active Probe	ZS1500
PCI Express X4 Express Card Host Interface for Laptop Express Card Slot	LSIB-HOSTCARD		1500-QUADPAK
PCI Express X4 3-meter Cable with X4 Cable	LSIB-CABLE-3M	WaveLink 7.5 GHz, Differential Probe Adjustable Tip Module	e D600A-AT*
Connectors Included PCI Express X4 7-meter Cable with X4 Cable	LSIB-CABLE-7M	WaveLink 3.5 GHz, ±2.5 V _{p-p} Differential Probe Small Tip Module	D310*
Connectors Included		WaveLink 3.5 GHz, ±5 V _{p-p} Differential Probe Small Tip Module	D320*
Mixed Signal Testing Options 500 MHz, 2 GS/s, 18 Ch, 50 Mpts/Ch	MS-500	WaveLink 6 GHz, ±2.5 V _{p-p} Differential Probe Small Tip Module	D610*
Mixed Signal Oscilloscope Option 250 MHz, 1 GS/s, 36 Ch, 25 Mpts/Ch (500 MHz, 10 Gb, 2 GC/s, 50 Metr/Ch letterlessed)	MS-500-36	WaveLink 6 GHz, ±5 V _{p-p} Differential Probe Small Tip Module	D620*
(500 MHz, 18 Ch, 2 GS/s, 50 Mpts/Ch Interleaved) Mixed Signal Oscilloscope Option		WaveLink 6 GHz, Differential Positioner Mounted Tip Modu	e D500PT*
250 MHz, 1 GS/s, 18 Ch, 10 Mpts/Ch	MS-250	WaveLink ProLink Probe Body	WL-PLink
Mixed Signal Oscilloscope Option	1410 200	WaveLink ProBus Probe Body	WL-PBus
		7.5 GHz Low Capacitance Passive Probe (÷10, 1 kΩ; ÷20, 5	00 Ω) PP066
General Purpose and Application Specific		1 GHz, Active Differential Probe (÷1, ÷10, ÷20)	AP034
Software Options		Optical-to-Electrical Converter, 500–870 nm ProLink	OE525
Advanced Customization Software Package	WPZi-XDEV	BMA Connector	
Spectrum Analyzer and Advanced FFT Option	WPZi-SPECTRUM	Optical-to-Electrical Converter, 950–1630 nm ProLink	OE555
EMC Pulse Parameter Software Package	WPZi-EMC	BMA Connector	+
Serial Data Mask Software Package	WPZi-SDM	10/100/1000Base-T Compliance Test Fixture	TF-ENET-B [†]
(Standard on SDA 7 Zi and DDA 7 Zi)		Telecom Adapter Kit 100 Ω Bal., 120 Ω Bal., 75 Ω Unbal.	TF-ET
Advanced Optical Recording Measurement Package	WPZi-AORM	SATA Gen1/Gen2 Compliance Test Fixture	TF-SATA
Demodulation Software Package	WPZi-DMOD	USB 2.0 Testing Compliance Test Fixture	TF-USB-B
Jitter Timing and Analysis Software Package (Standard on SDA7 Zi and DDA 7 Zi)	WPZi-JTA2	* For a complete probe, order a W-PLink or WL-PBus Probe Body with the Probe Tip Module	
Digital Filter Software Package	WPZi-DFP2	*	
Disk Drive Measurements Software Package (Standard on DDA 7 Zi)	WPZi-DDM2	† Includes ENET-2CAB-SMA018 and ENET-2ADA-BNCSMA	
Electrical Telecom Mask Test Software Package	WPZi-ET-PMT	A variety of other active voltage and current probes are all Consult LeCroy for more information.	so available.
General Accessories			
Top-mounted, Fully Integrated 15.3" WXGA with Touch Screen Display, Including all Cabling and Softwa	WPZi-EXTDISP-15 re	Customer Service LeCroy oscilloscopes and probes are designed, built, and	tested to
Keyboard, USB	KYBD-1	ensure high reliability. In the unlikely event you experience	
Probe Deskew and Calibration Test Fixture	TF-DSQ	our digital oscilloscopes are fully warranted for three years	s and our
Hard Carrying Case	WPZi-HARDCASE	probes are warranted for one year.	
Soft Carrying Case	WPZi-SOFTCASE	This warranty includes:	
Rackmount Accessory for Converting a Zi Series Oscilloscope to an 8U Rack-mounted Package	RACKMOUNT-1	No charge for return shipping	
ProLink to SMA Adapter	LPA-SMA-A	 Long-term 7-year support 	
IC: CD III COMMAND A	1 DA ON 4A 1/1T A	 Upgrade to latest software at no charge 	

LPA-SMA-KIT-A

OC1024

OC1021



Oscilloscope Cart

Kit of ProLink to SMA Adapters

Oscilloscope Cart with Additional Shelf and Drawer

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