Model 7225 DSP Lock-in Amplifier



FEATURES

- 0.001 Hz to 120 kHz operation
- Voltage and current mode inputs
- **Direct digital demodulation** without down-conversion
- 10 µs to 100 ks output time constants
- Quartz crystal stabilized internal oscillator
- Synchronous oscillator output for input offset reduction
- Harmonic measurements to 32f

APPLICATIONS

- Chopped light measurements
- AC bridge measurements
- Audio studies
- AC impedance studies
- Vibration studies
- Thermal wave detection

DESCRIPTION

The SIGNAL RECOVERY model 7225 offers a cost-effective solution to the researcher needing the performance provided by DSP demodulation but not requiring the additional features or higher operating frequencies of the models 7280 and 7265.

The instrument performs all of the normal measurements of a dual phase lock-in amplifier, measuring the in-phase and quadrature components, vector magnitude, phase angle and noise of the input signal.

Two auxiliary ADC inputs, four DAC outputs and eight output logic lines are provided. These can be used to record the magnitude of external signals associated with the experiment, such as temperature or pressure, or to generate voltages to control or switch other equipment. Information from the ADCs together with the lock-in amplifier's output data can be stored in the 32k point buffer memory prior to transfer back to a controlling computer.

The model 7225 is extremely easy to use. All instrument controls are adjusted via the left-hand display panel and its associated keys, while the right hand panel shows the two selected instrument outputs. Auto functions need only two keypresses to activate and in many cases eliminate the need for manual control adjustment.

External control of the unit is via either the RS232 or GPIB interfaces, using simple mnemonic-type ASCII commands. A second RS232 port allows up to sixteen 7225 or compatible instruments to be operated from a single RS232 computer port by connecting them in a "daisy-chain" configuration.

Compatible software is available in the form of a LabVIEW driver supporting all instrument functions, the Acquire lock-in amplifier applications software and the SRInstComms ActiveX control and software toolkit. The driver and a demonstration version of the applications software, DemoAcquire, are available for download from our website at www.signalrecovery.com

Specifications

General

Dual-phase DSP lock-in amplifier operating over a reference frequency range of 0.001 Hz to 120 kHz. Wide range of auxiliary inputs and outputs and user-upgradeable firmware.

Measurement Modes

The instrument can simultaneously show any two of these outputs on the front panel display:

Х	In-phase	
Y	Quadrature	
R	Magnitude	
θ	Phase Angle	
Noise	-	
Harmonic	<i>n</i> F, n ≤ 32F	
Noise		
Measures noise in a given bandwidth		

centered at the reference frequency F

Displays

Two 2-line 16 character backlit LCD panels giving digital indication of measured signals

Signal Channel

Voltage Input	
Modes	A only, -B only or
	Differential (A-B)
Full-scale Sensitivity	2 nV to 1 V in a
	1-2-5 sequence
Max. Dynamic Reserve	> 100 dB
Impedance	
FET Input	10 MΩ // 30 pF
Bipolar Input	10 kΩ // 30 pF
Maximum Safe Input	20 V pk-pk
Voltage Noise	
FET Input	5 nV/√Hz @ 1 kHz
Bipolar Input	2 nV/√Hz @ 1 kHz

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Model 7225 Specifications

	ecifications		
Voltage Input (continue	ed)	Demodulator and Out	tpu
C.M.R.R.	> 100 dB @ 1 kHz	Output Zero Stability	
Frequency Response	0.001 Hz to 120 kHz	Digital Outputs	No
Gain Accuracy	±0.2% typ	c .	se
Distortion	-90 dB THD (60 dB	Displays	No
	AC gain, 1 kHz)		se
Line Filter	attenuates 50, 60,	Analog Outputs	< !
	100, 120 Hz	Harmonic Rejection	-90
Grounding	BNC shields can be	Output Filters	
Crounding	grounded or floated	X and Y outputs only:	
	via 1 k Ω to ground	Time Constant	10
		Time Constant	bir
Current Input		Slope (roll-off)	6 0
Mode	Low Noise or Wide	All outputs	00
Mode	Low Noise or Wide Bandwidth	Time Constant	5 r
Full apple Consitivity	Bandwidth	Time Constant	1-2
Full-scale Sensitivity		Clana	
Low Noise	2 fA to 10 nA in a	Slope	6,
	1-2-5 sequence		oc
Wide Bandwidth	2 fA to 1 μA in a	Synchronous Filter	Av
	1-2-5 sequence		F٠
Max. Dynamic Reserve		Offset	Αι
Frequency Response (-	-3dB)		an
Low Noise	≥ 500 Hz		SC
Wide Bandwidth	≥ 50 kHz	Absolute Phase Measure	eme
Impedance			≤ (
Low Noise	< 2.5 kΩ @ 100 Hz		
Wide Bandwidth	< 250 Ω @ 1 kHz	Oscillator	
Noise	C	Frequency	
Low Noise	13 fA/√Hz @ 500 Hz	Range	0.0
Wide Bandwidth	1.3 pA/√Hz @ 1 kHz	Setting Resolution	
Gain Accuracy	± 0.6% typ, midband	$1 \text{ mHz} \leq F \leq 900$	Hz
Line Filter	attenuates 50, 60,	<i>F</i> > 900 Hz	
	100, 120 Hz	Absolute Accuracy	+ !
Grounding	BNC shield can be	Distortion (THD)	-80
Grounding	grounded or floated	Distortion (TTD)	10
	via 1 k Ω to ground	Amplitudo (rma)	10
		Amplitude (rms)	11
Deference Chennel		Range	
Reference Channel		Setting Resolution	
TTL Input (rear panel)		1 mV to 500 mV	11
Frequency Range	0.001 Hz to 120 kHz	500 mV to 2 V	41
Analog Input (front panel		2 V to 5 V	10
Impedance	1 MΩ // 30 pF	Accuracy	±0
Sinusoidal Input			±0
Level	1.0 V rms*	Stability	50
	0.3 Hz to 120 kHz	Output Impedance	50
Squarewave Input		Sweep (computer contro	ol c
Level	250 mV rms*	Amplitude Sweep	
Frequency Range	2 Hz to 120 kHz	Output Range	0.0
		Law	Lir
*Note: Lower levels	can be used with the	Step Rate	20
analog input at the	expense of increased		(5)
phase errors		Frequency Sweep	
·		Output Range	0.0
Phase Set Resolution	0.001° increments	Law	Lir
Phase Noise at 100 ms		Step Rate	20
Internal Reference	< 0.0001° rms		(5)
	< 0.01° rms @ 1 kHz		(0)
Orthogonality	90° ±0.0001°	Auxiliary Inputs	
Acquisition Time	30 10:0001	ADC 1 & 2	
Internal Reference	instantaneous	Maximum Input	±1
Internal Reference		•	1 r
	acquisition	Resolution	
External Reference		Accuracy	±2
Reference Frequency M		Input Impedance	11
	1 ppm or 1 mHz,	Sample Rate	
	whichever is the	ADC 1 only	40
	greater	ADC 1 and 2	17
		Trigger Mode	Int
			bu

ut Processing lo zero drift on all ettings lo zero drift on all ettings 5 ppm/°C 90 dB 0 µs to 640 µs in a inary sequence dB/octave msto 100 ks in a -2-5 sequence 12, 18 and 24 dB/ ctave vailable for < 20 Hz uto and Manual on X nd/or Y: ±300% fullcale ent Accuracy 0.01° 001 Hz to 120 kHz 1 mHz 7 4 mHz 50 ppm 30 dB @ 1 kHz and 00 mV rms mV to 5 V rms mV mV 0 mV $0.3\%, F \le 60 \text{ kHz},$ 0.5%, F > 60 kHz 0 ppm/°C 0Ω only) .000 to 5.000 V rms inear 0 Hz maximum 50 ms/step) .001 Hz to 120 kHz inear or Logarithmic 0 Hz maximum 50 ms/step) 10 V mV 20 mV MΩ // 30 pF 0 kHz max. 7.8 kHz max. nternal, External or burst Trigger Input TTL compatible

Outputs Fast Outputs Function Amplitude Impedance Update Rate Main Analog (CH1 and CH2) Outputs Function Amplitude Impedance Update Rate Signal Monitor Amplitude Impedance Auxiliary D/A Outputs 1, 2, 3 and 4 Maximum Output Resolution Accuracy Output Impedance 1 k Ω 8-bit Digital Output Port external equipment Reference Output Waveform Impedance Power - Low Voltage **Data Storage Buffer** Size etc. Max Storage Rate From LIA From ADC1 Interfaces General **Power Requirements** Voltage Frequency Power Dimensions Width Depth Height With feet Without feet Weight

X and Y ±2.5 V full-scale: linear to ±300% f.s. 1 kΩ 166 kHz X, Y, R, θ , Noise, Ratio, Log Ratio and User Equations 1 & 2. ±10.0 V full-scale; linear to ±120% fullscale 1 kO200 Hz ±10 V FS 1 kΩ +10 V 1 mV ±10 mV 8 TTL-compatible lines that can be independently set high or low to activate 0 to 5 V rectangular wave TTL-compatible ±15 V at 100 mA rear panel 5-pin 180° DIN connector for powering SIGNAL RECOVERY preamplifiers 32k × 16-bit data points, may be organized as 1×32k, 2×16k, 3×10.6k, 4×8k, up to 1000 16-bit values per second up to 40,000 16-bit values per second

RS232 and GPIB (IEEE-488). A second RS232 port is provided to allow "daisychain" connection and control of up to 16 units from a single RS232 computer port

> 110/120/220/240 VAC 50/60 Hz 40 VA max

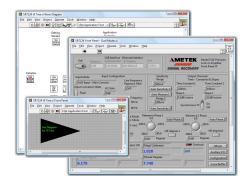
17" (432 mm) 16½" (415 mm)

3" (74 mm) 2¼" (60 mm) 16 lb (7.4 kg)

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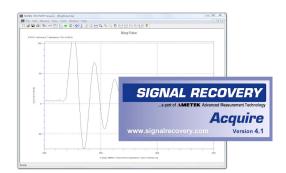
LabVIEW[®] Driver Software

A LabVIEW driver for the instrument is available from the **www.signalrecovery.com** website, offering example VIs for all its controls and outputs, as well as the usual Getting Started and Utility VIs. It also includes example soft-front panels built using these VIs, demonstrating how you can incorporate them in more complex LabVIEW programs.



SIGNAL RECOVERY Acquire Software (see page 56)

Those users who do not wish to write their own control code but who still want to record the instrument's outputs to a computer file will find the **SIGNAL RECOVERY** Acquire Lock-in Amplifier Applications Software, available at a small extra cost, useful. This 32-bit package, suitable for Windows XP/Vista, extends the capabilities of the instrument by, for example, adding the ability to record swept frequency measurements. It also supports the internal curve buffer, allowing acquisition rates of up to 1000 points per second independent of the computer's processor speed.



SRInstComms Software (see page 59)

Control up to ten **SIGNAL RECOVERY** instruments directly from Visual Basic, Visual C++, LabVIEW, Visual Basic for Applications (included in Word, Excel, Outlook, Access and other Microsoft products) and VBScript (supported by Internet Explorer 3 and later) without having to worry about low-level communications routines. The SRInstComms control handles all the communications between your software and the instrument(s) via the RS232 and/or GPIB interfaces, leaving you free to develop the code to run your experiment.



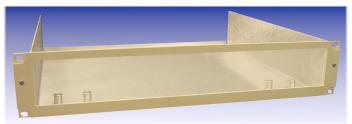
Ordering Information

Each model 7225 is supplied complete with a comprehensive instruction manual. Users may download the instrument's LabVIEW driver software and a free demonstration copy, DemoAcquire, of the **SIGNAL RECOVERY** lock-in amplifier applications software package, from the **www.signalrecovery.com** website.

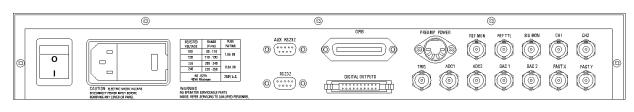
Optional Accessories

Model K02002

002 Rack mount to mount one model 7225 or 7225BFP in a 19" rack



Model K02002 Rack Mount Kit



Model 7225 Rear Panel Layout

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